



STIC Search Report

EIC 2800

STIC Database Tracking Number: 147452

TO: Jose Dees
Location: JEF-8D59
Art Unit : 2800
Thursday, March 10, 2005

Case Serial Number: 10/625706

From: Darcy Bates
Location: EIC 2800
JEF- 4B68
Phone: 571-272-2540

darcy.bates@uspto.gov

Search Notes

Re: 10/625,706 US 6,465,868

Attached are search results.

No U. S. litigation was found in searches of Lexis-Nexis and Questel-Orbit databases.

If more searching or explanation is needed, please let me know.

**Thanks,
Darcy Bates**

Bates, Darcy

From: Dees, Jose
Sent: Tuesday, March 08, 2005 5:37 PM
To: STIC-EIC2800
Subject: Reissue 10/625706

Please do a litigation search for Patent no. 6,465,868.

Thanks, J Dees

Day : Thursday
Date: 3/10/2005
Time: 12:20:24



PALM INTRANET

Application Number Information

Application Number: 10/625706 Assignments

Filing or 371(c) Date: 07/23/2003

Effective Date: 07/23/2003

Application Received: 07/24/2003

Patent Number:

Issue Date: 00/00/0000

Date of Abandonment: 00/00/0000

Attorney Docket Number: GR 98 P 5933

Status: 90 /ALLOWED -- NOTICE OF ALLOWANCE NOT YET MAILED

Confirmation Number: 8123

Examiner Number: 74643 / NGUYEN, CUONG

Group Art Unit: 2811

IFW IMAGE

Class/Subclass: 257/532.000

Lost Case: NO

Interference Number:

Unmatched Petition: NO

L&R Code: Secrecy Code:1

Third Level Review: NO

Secrecy Order: NO

Status Date: 07/27/2004

Title of Invention: INTEGRATED CIRCUIT HAVING CAPACITIVE ELEMENTS

Bar Code	PALM Location	Location Date	Charge to Loc	Charge to Name	Employee Name	Location
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Day : Thursday
Date: 3/10/2005
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 PALM INTRANET

Continuity Information for 10/625706

Parent Data

10625706

is a reissue of 09640150

Which is a continuation of PCT/DE99/03829 International Filing Date: 12/01/1999

Child Data

No Child Data

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Day : Thursday
 Date: 3/10/2005
 Time: 12:19:09


PALM INTRANET
Patent Number InformationApplication Number: **09/640150** AssignmentsFiling or 371(c) Date: **08/16/2000**Effective Date: **08/16/2000**Application Received: **08/17/2000**Patent Number: **6465868**Issue Date: **10/15/2002**Date of Abandonment: **00/00/0000**Attorney Docket Number: **GR 98 P 5933**Status: **150 / PATENTED CASE**Confirmation Number: **4425**Examiner Number: **74643 / NGUYEN, CUONG**Group Art Unit: **2811**Class/Subclass: **257/532.000**Lost Case: **NO**

Interference Number:

Unmatched Petition: **NO**L&R Code: Secrecy Code: **1**Third Level Review: **NO**Secrecy Order: **NO**Status Date: **09/26/2002**Title of Invention: **INTEGRATED CIRCUIT HAVING CAPACITIVE ELEMENTS**

Bar Code	PALM Location	Location Date	Charge to Loc	Charge to Name	Employee Name	Location
09640150	9210	03/09/2005	No Charge to Location	No Charge to Name	WYATT, DONNA	

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Day : Thursday
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Time: 12:20:10

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Continuity Information for 09/640150

Parent Data

09640150

is a continuation of PCT/DE99/03829 International Filing Date: 12/01/1999

Child Data

10625706 is a reissue of 09640150

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Litigation involving patent 6,465,868

Click on the docket number to view the docket.
Click on the above patent number to view the patent.

Docket Case Heading Date Filed Date Retrvd

There are no cases involving this patent number.

Source: [Legal > Area of Law - By Topic > Patent Law > Patents > U.S. Patents > Utility Patents](#)
Terms: [patno=6465868](#) ([Edit Search](#))

640150 (09) 6465868 October 15, 2002

UNITED STATES PATENT AND TRADEMARK OFFICE GRANTED PATENT

6465868

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[Link to Claims Section](#)

October 15, 2002

Integrated circuit having capacitive elements

REISSUE: July 23, 2003 - Reissue Application filed Ex. Gp.: 2811; Re. S.N. 10/625,706 (O.G. October 14, 2003)

INVENTOR: Ehben, Thomas - Munich, Germany (DE); Steinecke, Thomas - Hofsingelding, Germany (DE); Rosenbusch, Jens - Munich, Germany (DE)

APPL-NO: 640150 (09)

FILED-DATE: August 16, 2000

GRANTED-DATE: October 15, 2002

PRIORITY: December 16, 1998 - 19858114, Germany (DE)

ASSIGNEE-AT-ISSUE: Infineon Technologies AG, Munich, Germany (DE), 03

ASSIGNEE-AFTER-ISSUE: October 13, 2000 - ASSIGNMENT OF ASSIGNORS INTEREST (SEE DOCUMENT FOR DETAILS)., SIEMENS AKTIENGESELLSCHAFT ZT GG VM POSTFACH 22 16 34 MUNCHEN 80506 GERMANY, Reel and Frame Number: 11443/0884
July 25, 2003 - ASSIGNMENT OF ASSIGNORS INTEREST (SEE DOCUMENT FOR DETAILS)., INFINEON TECHNOLOGIES AG RIDLERSTRASSE 55 80339 MUNCHEN GERMANY, Reel and Frame Number: 14313/0089

LEGAL-REP: Greenberg, Laurence A.; Stemer, Werner H.; Mayback, Gregory L. - # #0

PUB-TYPE: October 15, 2002 - Utility Patent having no previously published pre-grant publication (B1)

PUB-COUNTRY: United States (US)

REL-DATA:

Continuation of Ser. No. PCT/DE99/03829, December 1, 1999, PENDING

US-MAIN-CL: 257#532

US-ADDL-CL: 257#533, 257#534, 257#E23.144, 257#E27.048


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patno=6465868

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and	and
or	or
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not w/N	not within N words
pre /N	precedes by N words
w/p	in same paragraph
not w/p	not in same paragraph
w/seg	in same segment
not w/seg	not in same segment
w/s	in same sentence
not w/s	not in same sentence
and not	and not

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Search statement 5

Query/Command : us6465868/pn

**** SS 5: Results 1**

Search statement 6

Query/Command : prt fu legalall max

1 / 1 PLUSPAT - ©QUESTEL-ORBIT - image
PN - US6465868 B1 20021015 [US6465868]
TI - (B1) Integrated circuit having capacitive elements
PA - (B1) INFINEON TECHNOLOGIES AG (DE)
PA0 - Infineon Technologies AG, Munich [DE]
IN - (B1) EHBEN THOMAS (DE); ROENBUSCH JENS (DE); STEINECKE THOMAS (DE)
AP - US64015000 20000816 [2000US-0640150]
FD - Cont. of 19991201 []
PR - DE19858114 19981216 [1998DE-1058114]
 WODE9903829 19991201 [1999WO-DE03829]
IC - (B1) H01L-027/108 H01L-029/00 H01L-029/76 H01L-029/94 H01L-031/119
EC - H01L-023/522C
 H01L-023/66
 H01L-027/08C
PCL - ORIGINAL (O) : 257532000; CROSS-REFERENCE (X) : 257E23144
 257E27048 257533000 257534000
DT - Corresponding document
CT - US5396198; US5606197; US6146939; DE4322354; EP0163384; EP0656657;
 EP0908950; JP02-250370; JP04127464; JP09-246476
STG - (B1) U.S. Patent (no pre-grant pub.) after Jan. 2, 2001
AB - An integrated circuit having capacitive elements for smoothing a supply voltage is described. In this case, at least one additional metal electrode, which is configured as a high frequency-optimized capacitance and is distinguished by an extremely low sheet resistance, is connected in parallel with the MOS capacitances. By connecting the areally highly effective MOS capacitance, which, however, is connected with a somewhat higher impedance, in parallel with areally less effective metal capacitances, which, however, are connected to the supply voltage in a very low-impedance manner, it is possible to obtain broadband buffering and thus decoupling of high-frequency interference signals. Very high-frequency interference components are attenuated on the chip and do not pass into the system surrounding the integrated circuit.
UP - 2002-43

1 / 1 LGST - ©EPO
PN - US6465868 B1 20021015 [US6465868]
AP - US64015000 20000816 [2000US-0640150]
ACT - 20001013 US/AS-A
 ASSIGNMENT
 OWNER: SIEMENS AKTIENGESELLSCHAFT ZT GG VM POSTFACH 22 16
 ASSIGNMENT OF ASSIGNORS INTEREST;ASSIGNORS:EHBEN,
 THOMAS;STEINECKE, THOMAS;ROENBUSCH,
 JENS;REEL/FRAME:011443/0884;SIGNING DATES FROM 20000901 TO

3/10/05

US 6,465,868

10/625706

20000912

20030725 US/AS-A

ASSIGNMENT

OWNER: INFINEON TECHNOLOGIES AG RIDLERSTRASSE 5580339 MUN;

EFFECTIVE DATE: 20030526

ASSIGNMENT OF ASSIGNORS INTEREST;ASSIGNOR:SIEMENS
AKTIENGESELLSCHAFT;REEL/FRAME:014313/0089

20031014 US/RF-A

REISSUE APPLICATION FILED

EFFECTIVE DATE: 20030723

UP - 2005-03

I / 1 CRXX - ©CLAIMS/RRX

AN - 3768666

PN - 6,465,868 A 20021015 [US6465868]

PA - Infineon Technologies AG DE

PT - E (Electrical)

ACT - 20030723 REISSUE REQUESTED

ISSUE DATE OF O.G.: 20031014

REISSUE REQUEST NUMBER: 10/625706

EXAMINATION GROUP RESPONSIBLE FOR REISSUEPROCESS: 2811

Reissue Patent Number:

20030725 REASSIGNED

ASSIGNMENT OF ASSIGNORS INTEREST

Assignor: SIEMENS AKTIENGESELLSCHAFT, DATE SIGNED: 05/26/2003

Assignee: INFINEON TECHNOLOGIES AG, RIDLERSTRASSE 55, 80339
MUNCHEN, GERMANY

Reel 014313/Frame 0089

Contact: LERNER AND GREENBERG, WERNER H. STEMER, P.O. BOX 2480,
HOLLYWOOD, FL 33022

UP - 2003-42

UACT- 2003-10-14

URAS- 2004-03-02

Search statement 6

Query/Command : fam us6465868/pn

1 Patent Groups
** SS 6: Results 5

STIC EIC 2800

Darcy Bates

272-2540

Search statement 7

Query/Command : famstate nonstop

1 / 5 *PLUSPAT - ©QUESTEL-ORBIT*
PN - CN1291352 A 20010411 [CN1291352]
STG - (A) Unexamined application
TI - (A) Integrated circuit with capacity element
PA - (A) INFINEON TECHNOLOGY CO LTD (DE)
IN - (A) SCHTANECK T (DE); EBEN T (DE); ROSEN BASCH J (DE)
IC - (A) H01L-023/522 H01L-027/08
AP - CN99803004 19991201 [1999CN-0803004]
PR - DE19858114 19981216 [1998DE-1058114]
UP - 2005-08

2 / 5 *PLUSPAT - ©QUESTEL-ORBIT*
PN - EP1057217 A1 20001206 [EP1057217]
STG - (A1) Public. Of applic. With search report
TI - (A1) INTEGRATED CIRCUIT WITH CAPACITIVE ELEMENTS
OTI - (A1) INTEGRIERTE SCHALTUNG MIT KAPAZITIVEN ELEMENTEN
(A1) CIRCUIT INTEGRE A ELEMENTS CAPACITIFS
PA - (A1) INFINEON TECHNOLOGIES AG (DE)
IN - (A1) EHBEN THOMAS (DE); ROEN BUSCH JENS (DE); STEINECKE THOMAS (DE)
IC - (A1) H01L-023/522 H01L-027/08
LA - GERMAN (GER)
AP - EP99966817 19991201 [1999EP-0966817]
PR - WODE9903829 19991201 [1999WO-DE03829]
DE19858114 19981216 [1998DE-1058114]
DS - AT BE CH CY DE DK ES FI FR GB GR IE IT LI LU MC NL PT SE
UP - 2000-49

1 / 1 *LEGALI - ©EPO*
PN - EP1057217 A1 20001206 [EP1057217]
AP - EP99966817 19991201 [1999EP-0966817]
ACTE - 20001206 EP/AK-A [+]
DESIGNATED CONTRACTING STATES:
AT BE CH CY DE DK ES FI FR GB GR IE IT LI LU MC NL PT SE
20001206 EP/17P-A [+]
REQUEST FOR EXAMINATION FILED
EFFECTIVE DATE: 20000519
20040519 EP/RBV-A [+]
DESIGNATED CONTRACTING STATES (CORRECTION):
DE FR GB IT
UP - 2004-21

3 / 5 *PLUSPAT - ©QUESTEL-ORBIT*
PN - JP2002532903 T 20021002 [JP2002532903]
STG - (T) Unexam. Pat. Appl. On foreign appl.
IC - (T) H01L-021/822 H01L-021/8234 H01L-027/04 H01L-027/06
AP - JP2000588808T 19991201 [2000JP-0588808]
PR - DE19858114 19981216 [1998DE-1058114]

3/10/05

US 6,465,868

10/625706

WODE9903829 19991201 [1999WO-DE03829]
UP - 2002-45

4 / 5 PLUSPAT - ©QUESTEL-ORBIT - image
PN - US6465868 B1 20021015 [US6465868]
STG - (B1) U.S. Patent (no pre-grant pub.) after Jan. 2, 2001
TI - (B1) Integrated circuit having capacitive elements
PA - (B1) INFINEON TECHNOLOGIES AG (DE)
PAO - Infineon Technologies AG, Munich (DE)
IN - (B1) EHBEN THOMAS (DE); ROSENBUSCH JENS (DE); STEINECKE THOMAS (DE)
IC - (B1) H01L-027/108 H01L-029/00 H01L-029/76 H01L-029/94 H01L-031/119
AP - US64015000 20000816 [2000US-0640150]
PR - DE19858114 19981216 [1998DE-1058114]
WODE9903829 19991201 [1999WO-DE03829]
EC - H01L-023/522C
H01L-023/66
H01L-027/08C
PCL - ORIGINAL (O) : 257532000; CROSS-REFERENCE (X) : 257E23144
257E27048 257533000 257534000
DT - Corresponding document
UP - 2002-43

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PN - US6465868 B1 20021015 [US6465868]
AP - US64015000 20000816 [2000US-0640150]
ACTE - 20001013 US/AS-A
ASSIGNMENT
OWNER: SIEMENS AKTIENGESELLSCHAFT ZT GG VM POSTFACH 22 16
ASSIGNMENT OF ASSIGNORS INTEREST;ASSIGNORS:EHBEN,
THOMAS;STEINECKE, THOMAS;ROSENBUSCH,
JENS;REEL/FRAME:011443/0884;SIGNING DATES FROM 20000901 TO
20000912

20030725 US/AS-A
ASSIGNMENT
OWNER: INFINEON TECHNOLOGIES AG RIDLERSTRASSE 5580339 MUN;
EFFECTIVE DATE: 20030526
ASSIGNMENT OF ASSIGNORS INTEREST;ASSIGNOR:SIEMENS
AKTIENGESELLSCHAFT;REEL/FRAME:014313/0089

20031014 US/RF-A
REISSUE APPLICATION FILED
EFFECTIVE DATE: 20030723
UP - 2005-03

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PN - WO200036651 A1 20000622 [WO200036651]
STG - (A1) Publ. Of int. Appl. With int. Search rep
TI - (A1) INTEGRATED CIRCUIT WITH CAPACITIVE ELEMENTS
OTI - (A1) CIRCUIT INTEGRE A ELEMENTS CAPACITIFS
 (A1) INTEGRIERTE SCHALTUNG MIT KAPAZITIVEN ELEMENTEN
PA - (A1) INFINEON TECHNOLOGIES AG (DE); EHBEN THOMAS (DE); STEINECKE
 THOMAS (DE); ROSENBUSCH JENS (DE)
PAO - INFINEON TECHNOLOGIES AG ; St.-Martin-Str. 53 D-81541 München (DE)
 (except US)
 EHBEN, Thomas ; Aidenbachstr. 118 D-81379 München (DE) (only US)
 STEINECKE, Thomas ; Am Hof 14 D-85457 Hofsgelding (DE) (only US)

3/10/05

US 6,465,868

10/625706

ROSENBUSCH, Jens ; Ludwig-Erhard-Allee 25 D-81739 München (DE)
(only US)

IN - (A1) EHBEN THOMAS (DE); STEINECKE THOMAS (DE); ROSENBUSCH JENS (DE)

IC - (A1) H01L-023/522 H01L-027/08

LA - GERMAN (GER)

AP - WODE9903829 19991201 [1999WO-DE03829]

PR - DE19858114 19981216 [1998DE-1058114]

EC - H01L-023/522C
H01L-023/66
H01L-027/08C

DS - CN; JP; KR; US; European Patent (AT; BE; CH; CY; DE; DK; ES; FI;
FR; GB; GR; IE; IT; LU; MC; NL; PT; SE)

DT - Basic

UP - 2000-26

I / 1 LEGALI - ©EPO
PN - WO200036651 A1 20000622 [WO200036651]
AP - WODE9903829 19991201 [1999WO-DE03829]

ACTE- 20000622 WO/AK [+]
DESIGNATED STATES CITED IN A PUBLISHED APPLICATION WITH SEARCH
REPORT
CN JP KR US

20000622 WO/AL [+]
DESIGNATED COUNTRIES FOR REGIONAL PATENTS CITED IN A PUBLISHED
APPLICATION WITH SEARCH REPORT
AT BE CH CY DE DK ES FI FR GB GR IE IT LU MC NL PT SE

20000811 WO/ENP
ENTRY INTO THE NATIONAL PHASE IN:
KR 2000 2000708812A [2000KR-0708812]

20000816 WO/121
EP: THE EPO HAS BEEN INFORMED BY WIPO THAT EP WAS DESIGNATED IN
THIS APPLICATION

20000816 WO/ENP
ENTRY INTO THE NATIONAL PHASE IN:
US 2000 640150A 20000816 [2000US-0640150]

UP - 2003-22


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Syntax Definition

and	and
or	or
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not w/N	not within N words
pre /N	precedes by N words
w/p	in same paragraph
not w/p	not in same paragraph
w/seg	in same segment
not w/seg	not in same segment
w/s	in same sentence
not w/s	not in same sentence
and not	and not

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Syntax Definition

and	and
or	or
w/N	within N words
not w/N	not within N words
pre /N	precedes by N words
w/p	in same paragraph
not w/p	not in same paragraph
w/seg	in same segment
not w/seg	not in same segment
w/s	in same sentence
not w/s	not in same sentence
and not	and not

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[ECLIPSE](#) | [History](#) | [Help](#)
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 Terms and Connectors Natural Language

6465868 or 6,465,868

[Suggest Terms for My Search](#)

Search Connectors

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Syntax Definition

<u>and</u>	and
<u>or</u>	or
<u>w/N</u>	within N words
<u>not w/N</u>	not within N words
<u>pre /N</u>	precedes by N words
<u>w/p</u>	in same paragraph
<u>not w/p</u>	not in same paragraph
<u>w/seg</u>	in same segment
<u>not w/seg</u>	not in same segment
<u>w/s</u>	in same sentence
<u>not w/s</u>	not in same sentence
<u>and not</u>	and not

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